Abstracts

Chirped delay lines in microstrip technology

M.A.G. Laso, T. Lopetegi, M.J. Erro, D. Benito, M.J. Garde, M.A. Muriel, A. Sorolla and M. Guglielmi. "Chirped delay lines in microstrip technology." 2001 Microwave and Wireless Components Letters 11.12 (Dec. 2001 [MWCL]): 486-488.

In this paper, we report on a design method for chirped delay lines (CDLs) in microstrip technology. They consist in a continuously varying strip width, so that the coupling location between the quasi-TEM microstrip mode and the same but counter-propagating mode is linearly distributed in frequency. High delay/spl times/bandwidth products, over frequency ranges of several gigahertzs, can be obtained following this procedure. Experimental data confirm the design method. Real-time Fourier analysis of wideband pulses can be performed using these CDLs.

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